

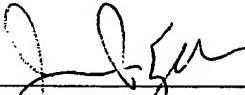
IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE

#4 Elect  
8/1/03  
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Applicant: Suk-Kyun Lee )  
Serial No.: 10/045,350 )  
Filed: November 9, 2001 )  
For: "Semiconductor Element and )  
Manufacturing Method Thereof" )  
Group Art Unit: 2818 )  
Examiner: Dao H. Nguyen )

I hereby certify that this paper (or fee) is being deposited with the United States Postal Service, first class postage prepaid, addressed to: Commissioner for Patents, Washington, D.C. 20231

February 27, 2003

  
James P. Zeller  
Reg. No. 28,491

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

This paper is in response to the official action of February 10, 2003, wherein restriction was required between device claims 1-10 and method claims 11-17.

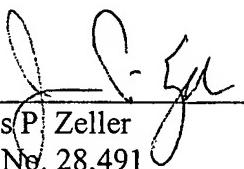
In response, applicant elects device claims 1-10 for prosecution, without traverse.

Please cancel claims 11-17 without prejudice.

Should the examiner wish to discuss the foregoing or any matter of form in an effort to advance this application towards allowance, he is urged to telephone the undersigned at the indicated number.

Respectfully submitted,

MARSHALL, GERSTEIN & BORUN

By: 

James P. Zeller  
Reg. No. 28,491  
Attorney for Applicant

**BEST AVAILABLE COPY**

February 27, 2003

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